

FIG. 1

Related Art

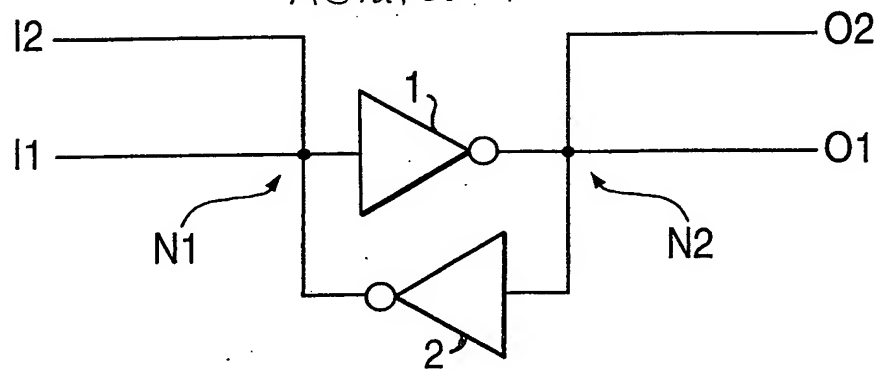


FIG. 2

Related Art

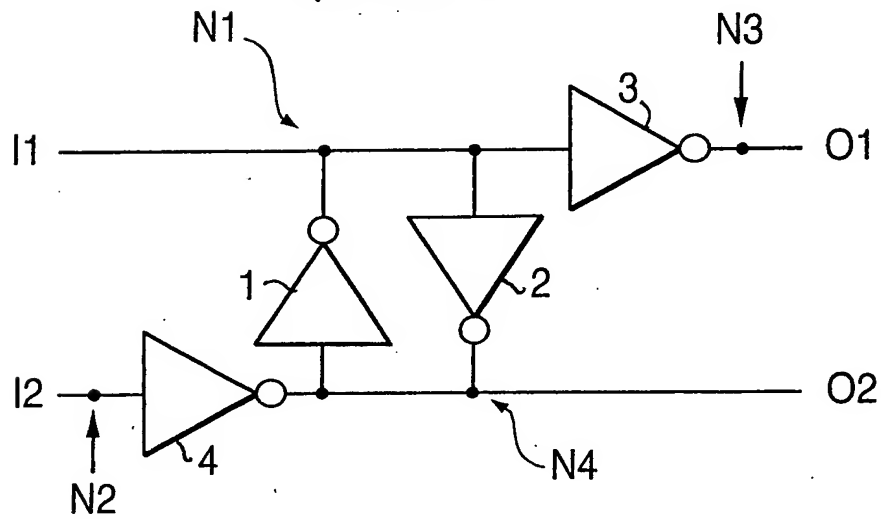
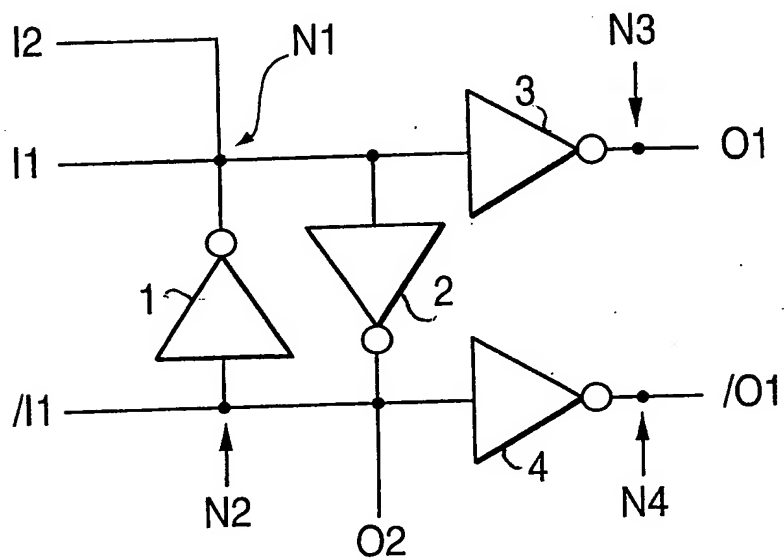


FIG. 3
Related Art



9	CELL ARRAY	7	MAIN DE- CODER	8	SENSE AMPLIFIER WRITE AMPLIFIER INPUT/OUTPUT BUFFER
				6	PRE- DECODER
				5	ADDRESS INPUT LATCH



FIG. 5

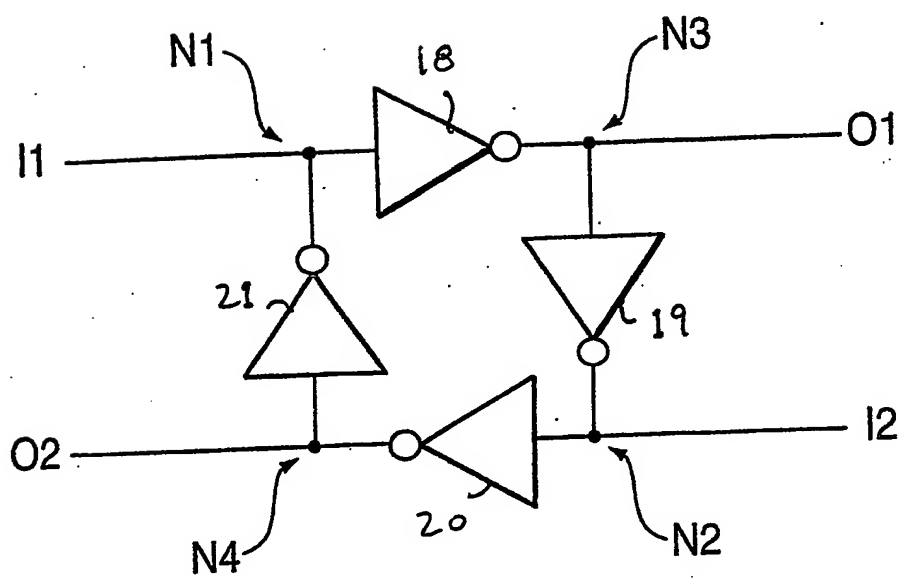


FIG. 6

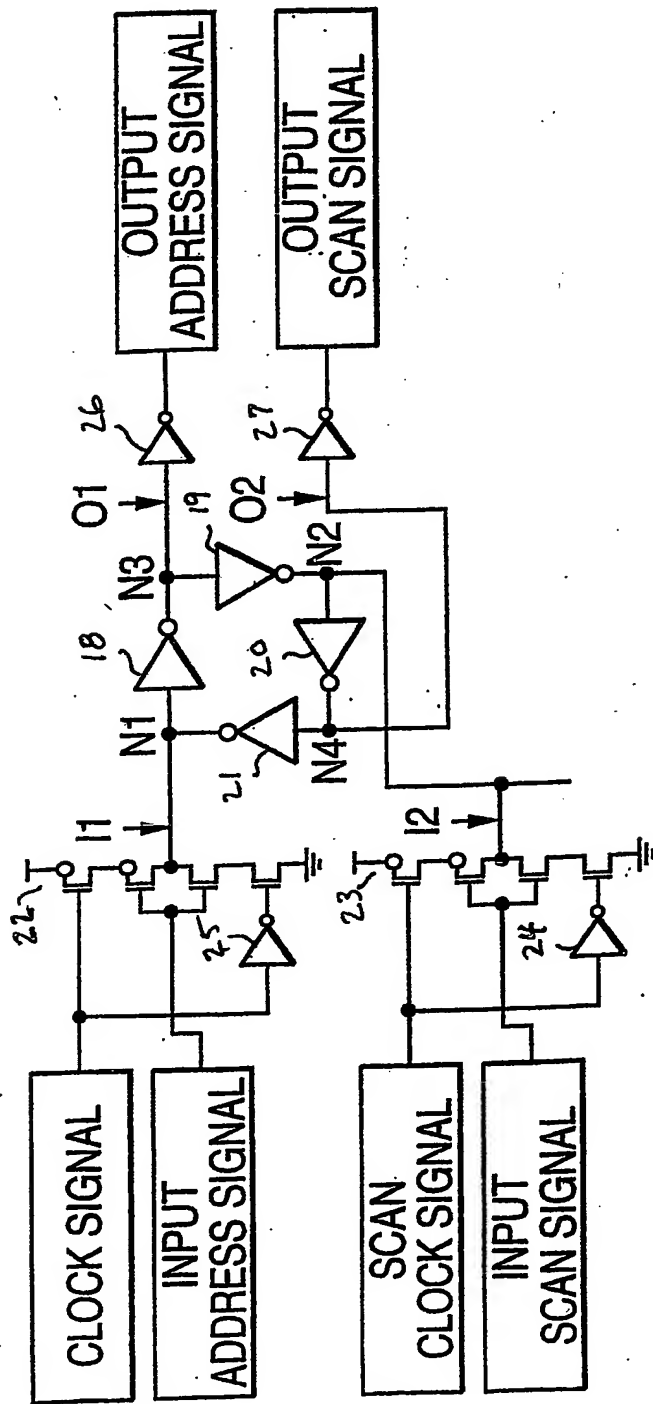


FIG. 7

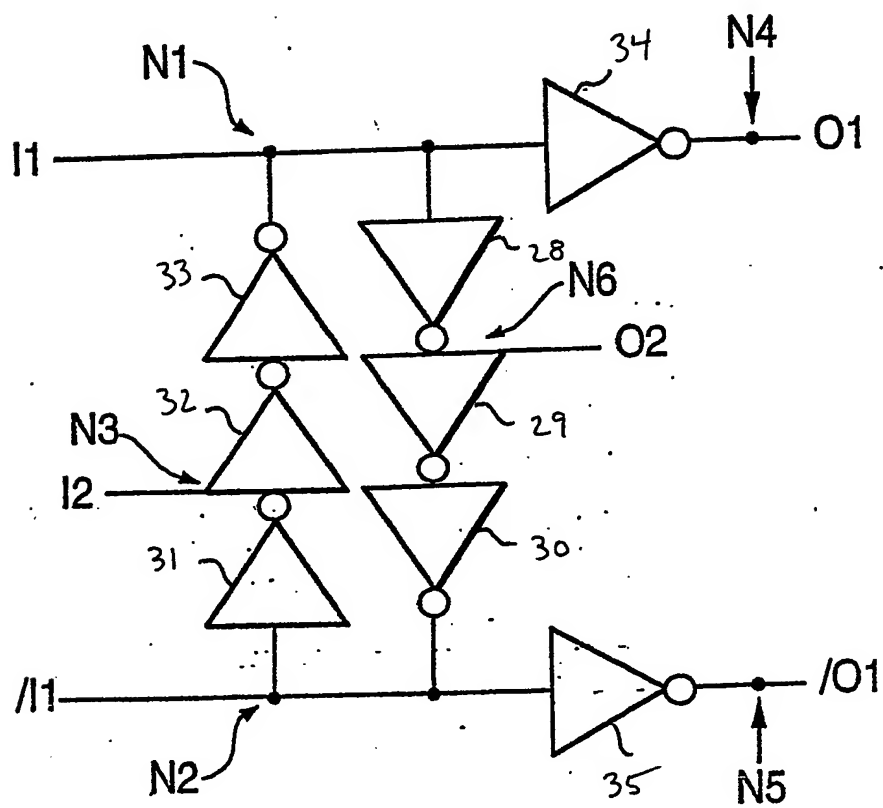


FIG. 8

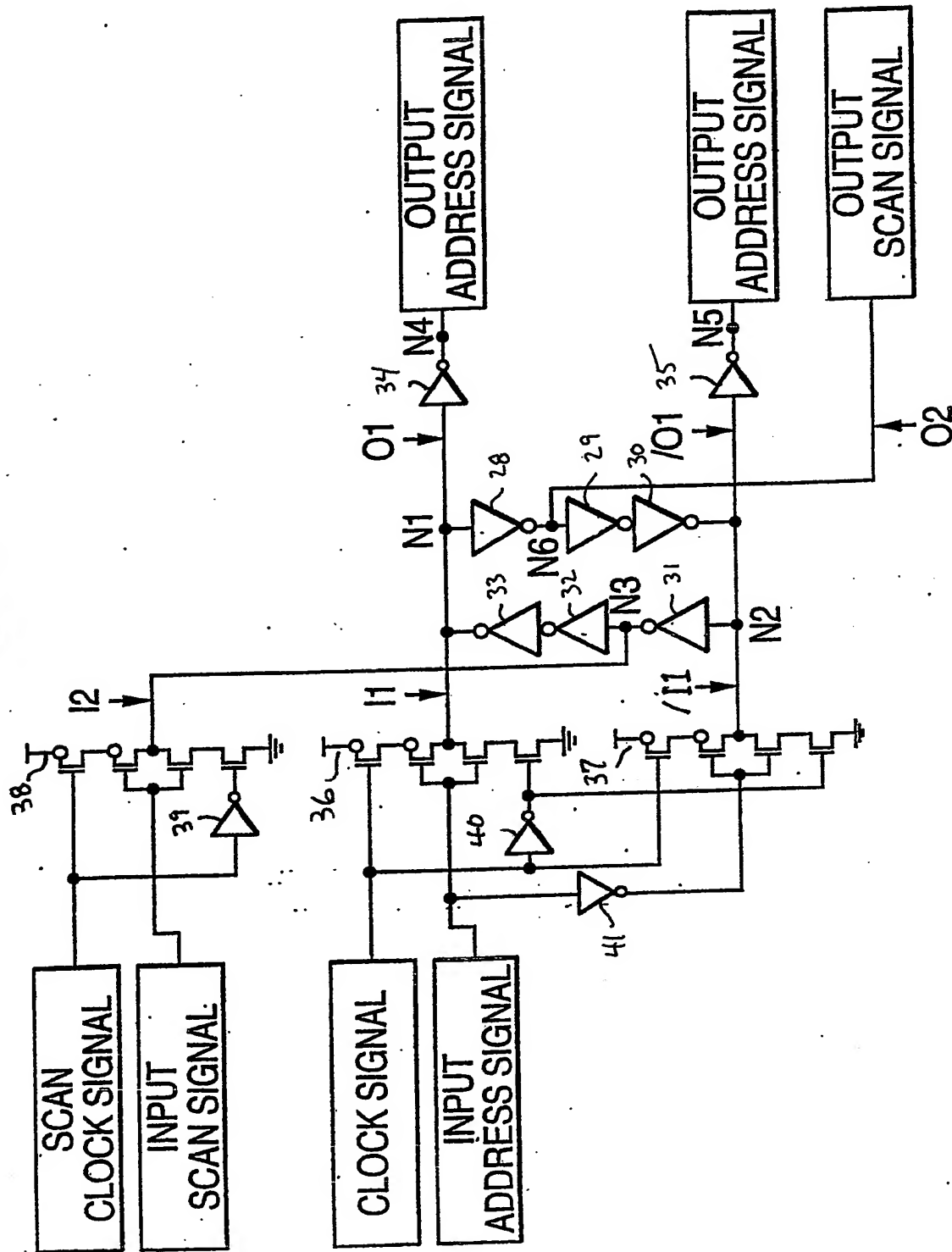


FIG. 9

